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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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09/529,597 05/31/00 ASAI

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MM91/0913

EXAMINER

BUI, H

ART UNIT

PAPER NUMBER

2841

DATE MAILED:

09/13/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

# Office Action Summary

Application No.

09/529,597

Applicant(s)

ASAI ET AL.

Examiner

Hung S Bui

Art Unit

2841

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1 is/are pending in the application.
- 4a) Of the above claim(s) 2-8 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claims \_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on \_\_\_\_ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. § 119

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

## Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892)
- 16) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 17) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 7 and 14.
- 18) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_.
- 19) ☐ Notice of Informal Patent Application (PTO-152)
- 20) ☐ Other:

## DETAILED ACTION

### *Election/Restrictions*

1. Applicant's election without traverse of claim 1 in Paper No. 15 is acknowledged.
2. Claims 2-8 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected species, there being no allowable generic or linking claim. Election was made **without** traverse in Paper No. 15.

### *Claim Rejections - 35 USC § 103*

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kinoshita [US 6,127,633] in view of Miles et al. [US 5,535,101] and Arai et al. [US 5,315,072].

**Regarding claim 1**, Kinoshita discloses a package board (1) having a core board (2) on each surface of which a plurality of conductor circuits (3) are formed with an interlaminar resin insulating layer (40) therebetween, a plurality of conductors (7) are formed on the IC chip mounted side surface.

Kinoshita disclose the instant claimed invention except for: the specifics of the conductor mounting to an integrated circuit chip and the use of a dummy circuit beneath the interlaminar layer.

Miles et al. disclose a printed circuit board (16) having a first surface with a plurality of soldering pads (26) mounted thereon and a second opposing surface with a

plurality of solderballs (27) mounted thereon, wherein the solder pads on the second surface have a larger surface than those of the first (see figure 2).

It would have been obvious to a person having ordinary skill in the art at the time the invention was made to use solder pad design of Miles et al. in Kinoshita for the purpose of facilitating mounting of the board.

Arai discloses a board mountable circuit board for mounting an integrated circuit having an intermediate layer including a dummy pattern (27) formed on the IC mounting surface of the printed circuit board.

It would have been obvious to a person having ordinary skill in the art at the time the invention was made to use the dummy pattern within the circuit board of Kinoshita, as modified by Miles et al., for the purpose of interconnecting the inner and outer layers of the integrated circuit.

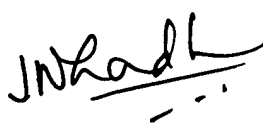
### ***Conclusion***

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung S Bui whose telephone number is (703) 305-8024. The examiner can normally be reached on Monday-Friday 8:30AM-5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey Gaffin can be reached on (703) 308-3301. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 305-0341 for regular communications and (703) 305-0341 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-0956.

HB  
9/5/01

  
**Jayprakash N. Gandhi**  
**Primary Examiner**  
**Technology Center 2800**